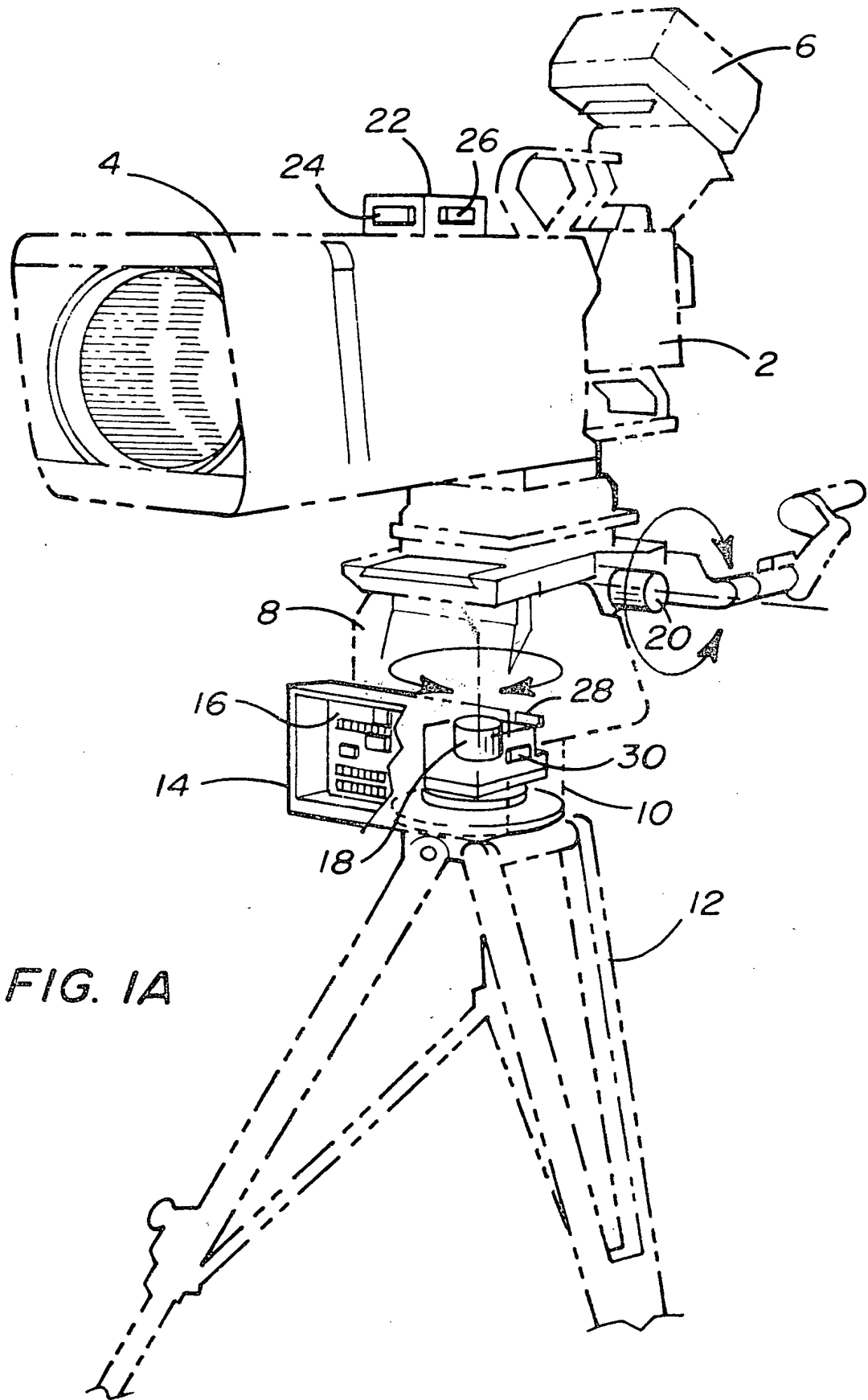


662627 662627 662627



03472535 122799

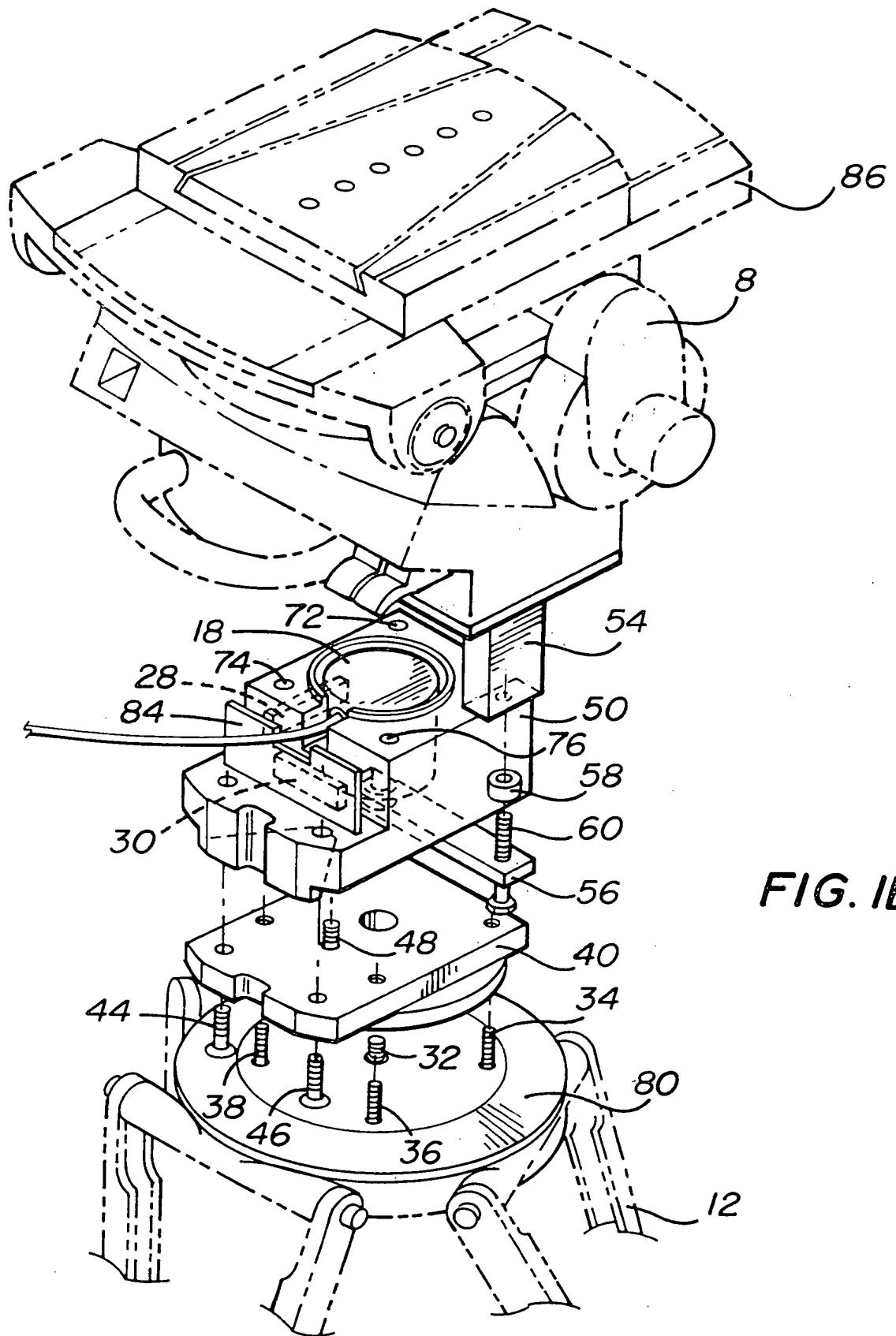


FIG. 1B

662227 662227 662227

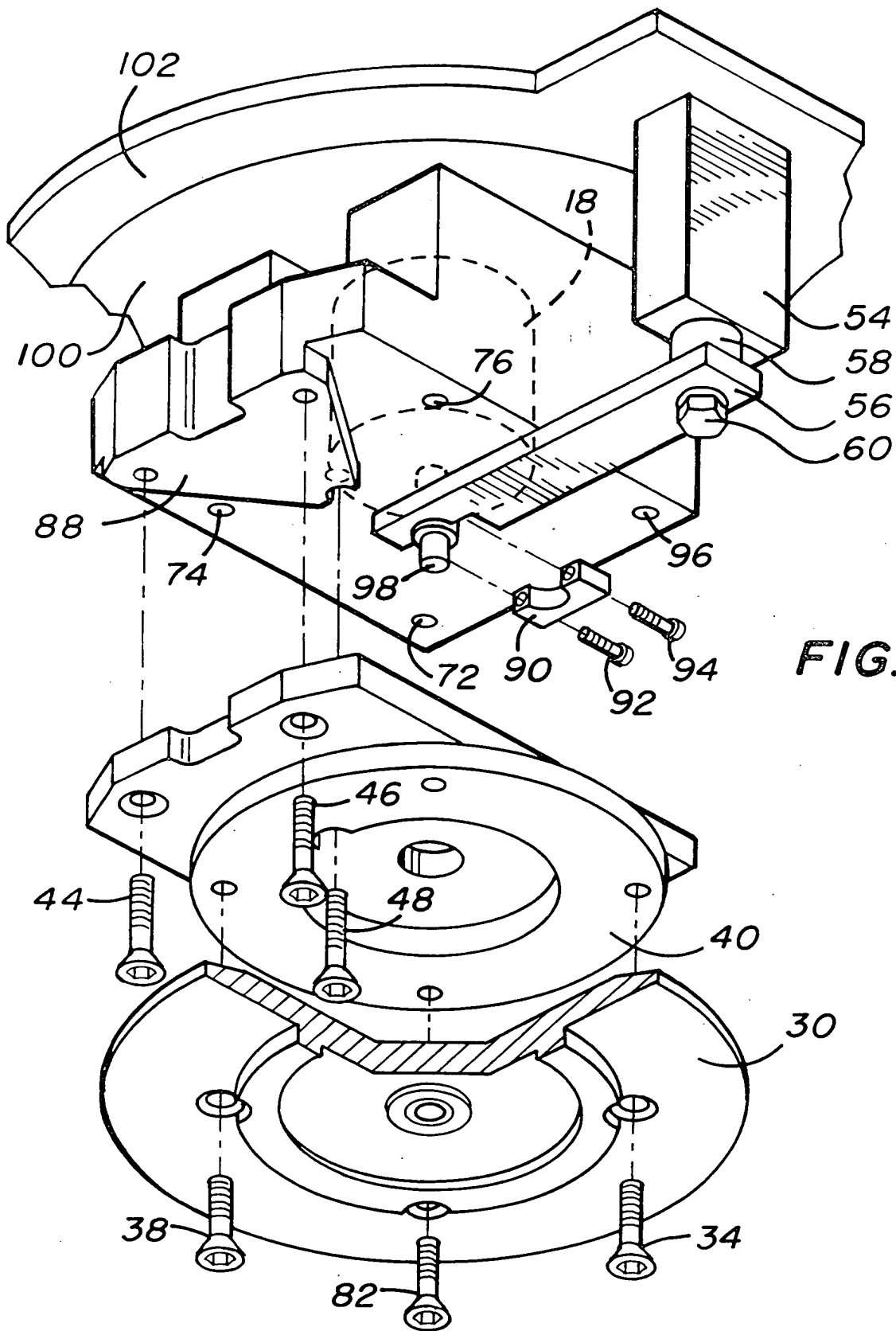


FIG. 1C

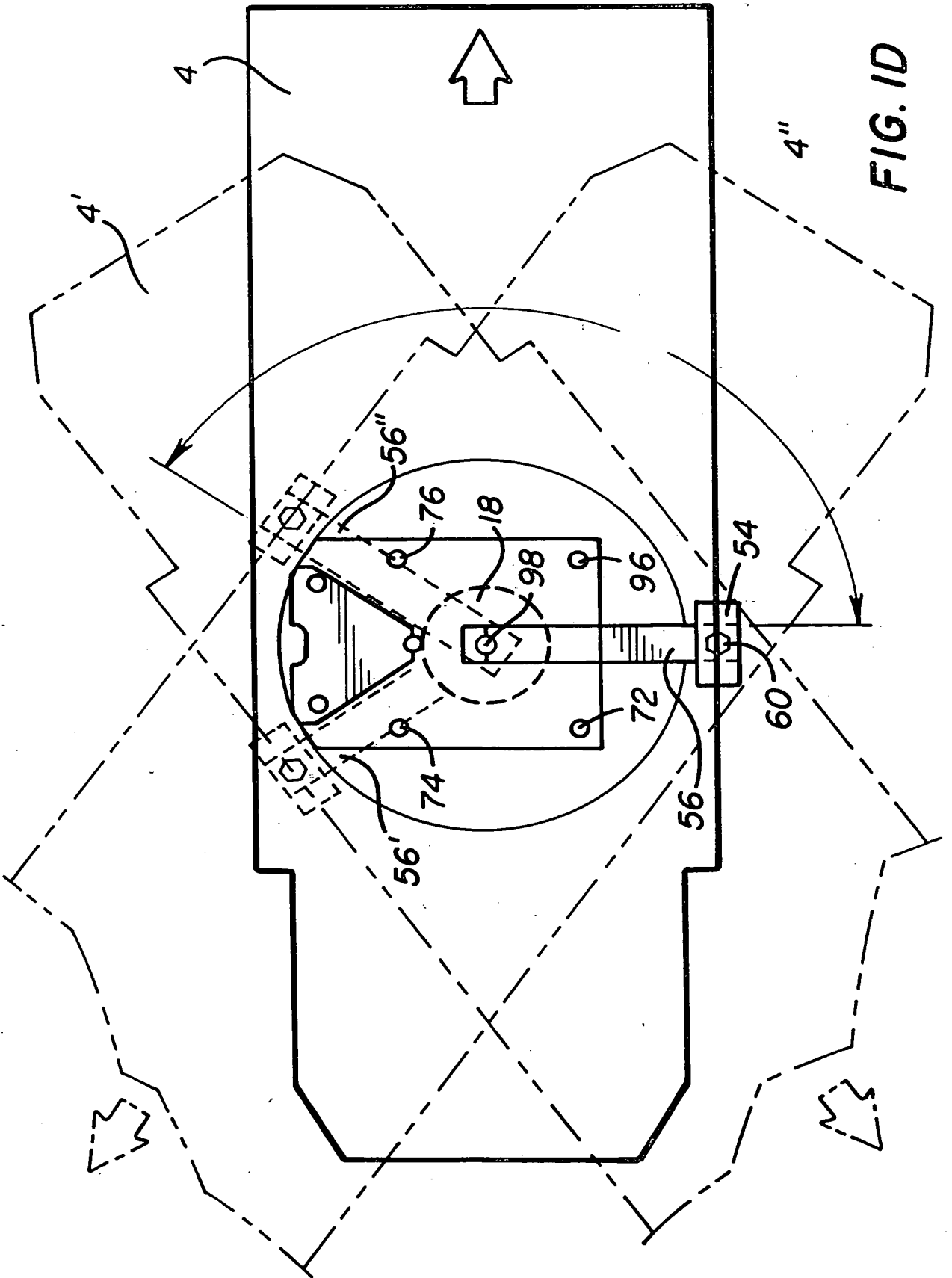


FIG. 1D

Fig. 2 is a block diagram of a video camera system. The system includes a pan encoder (18), a tilt encoder (20), two gyro sensors (24, 26), and two inclinometers (28, 30). These sensors are connected to an FPGA (212) and an A/D converter (214). The FPGA is also connected to a processor (216), which is linked to data memory (236) and program memory (238). The processor is connected to a modulator (244), which is connected to an audio driver (246) and a coax driver (248). A sync decoder (240) is also connected to the system. The entire system is enclosed in a box labeled 16.

Fig. 2

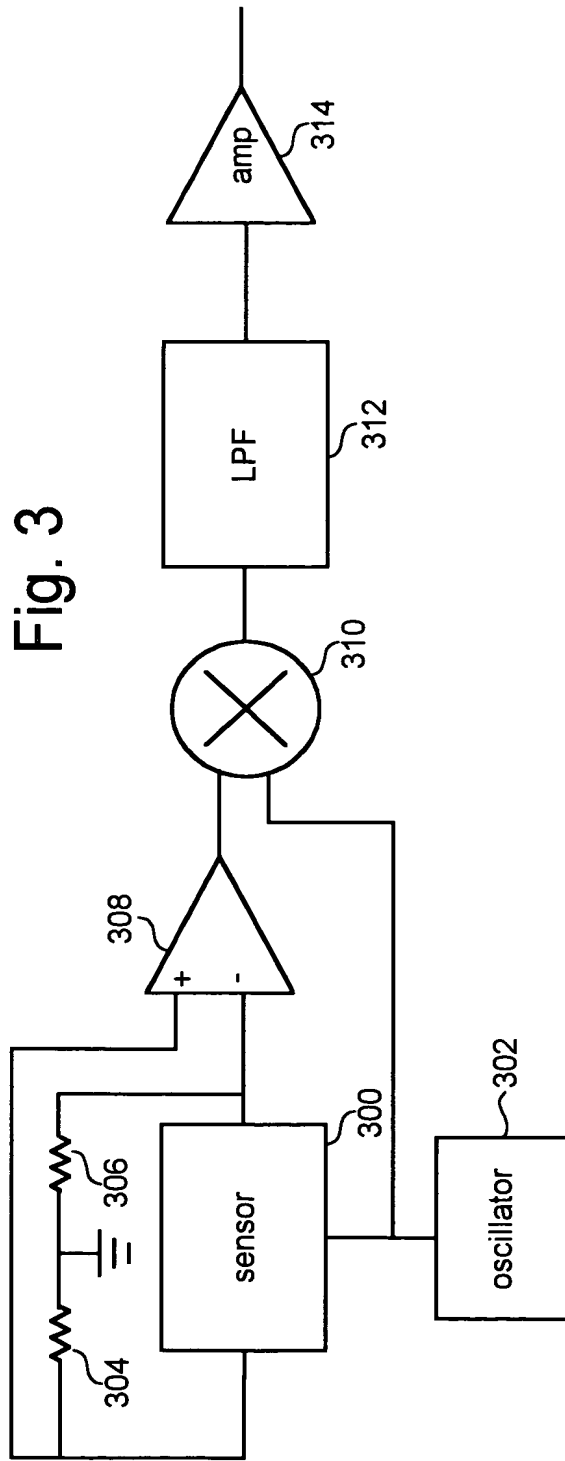


Fig. 3

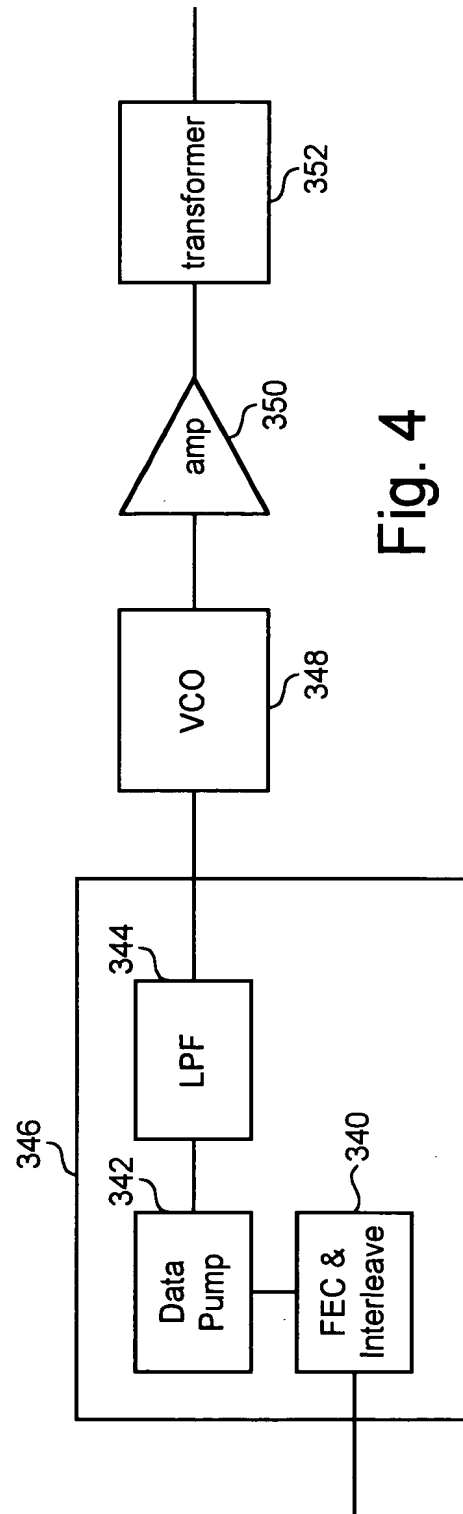


Fig. 4

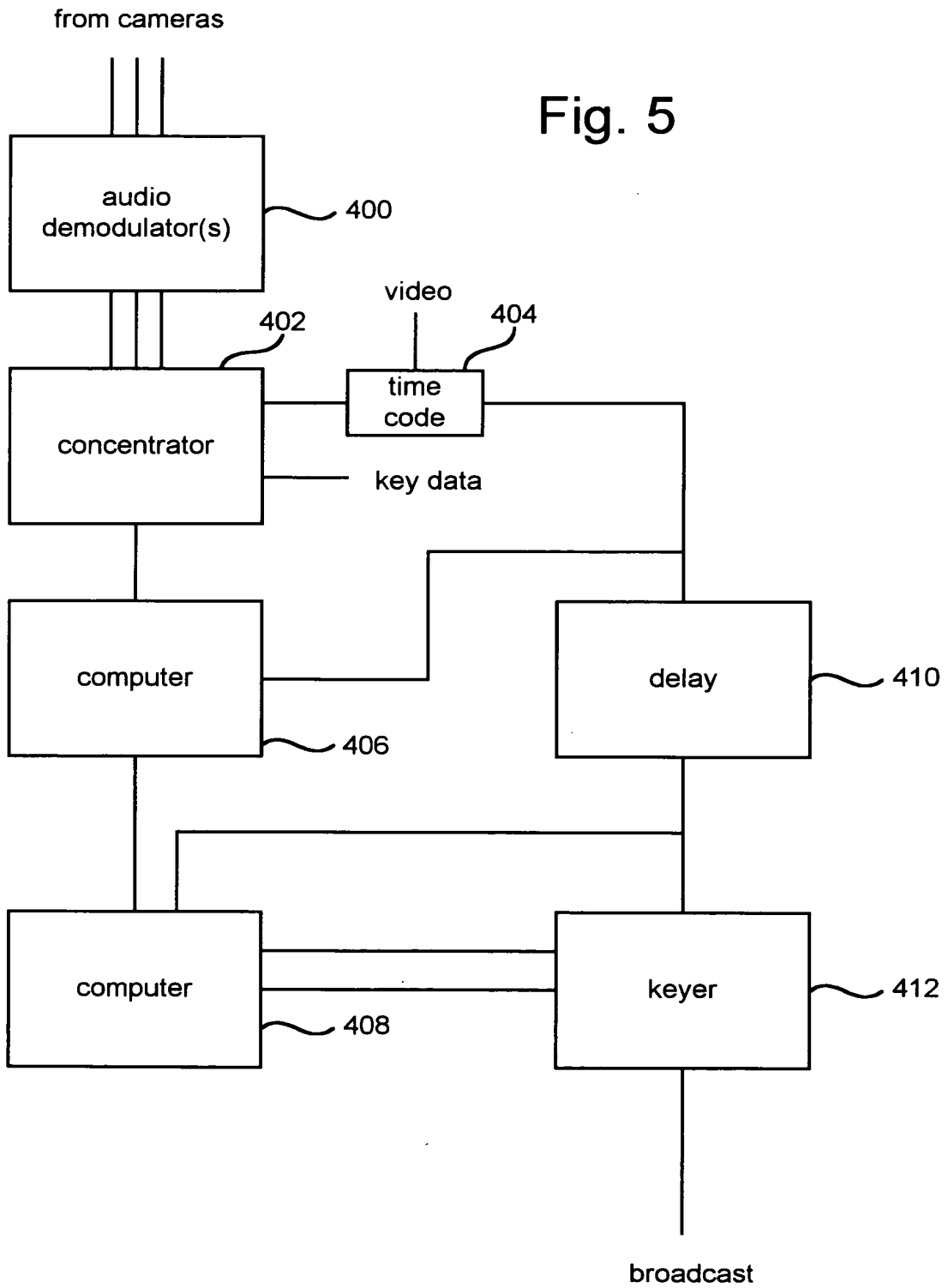


Fig. 6

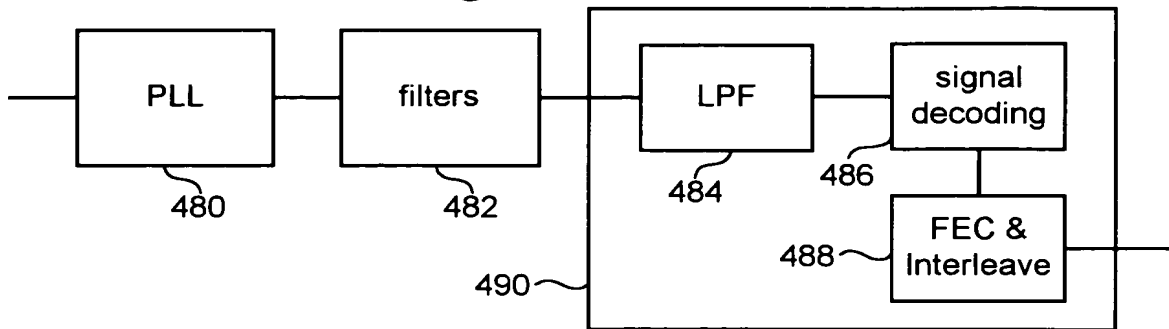


Fig. 7

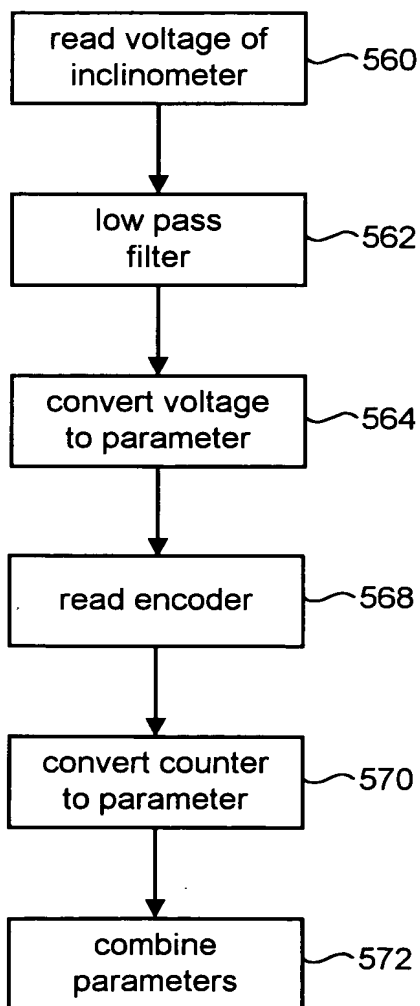


Fig. 11

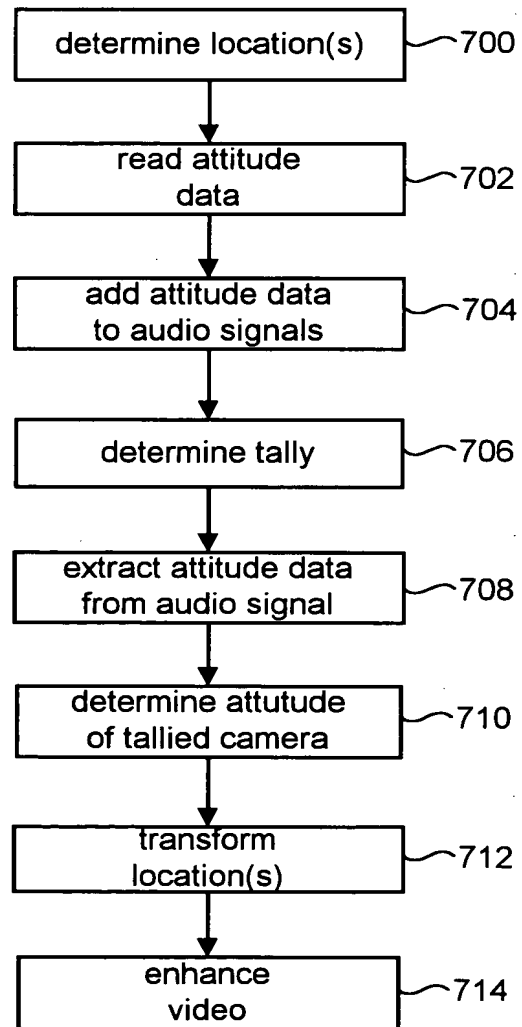


Fig. 8

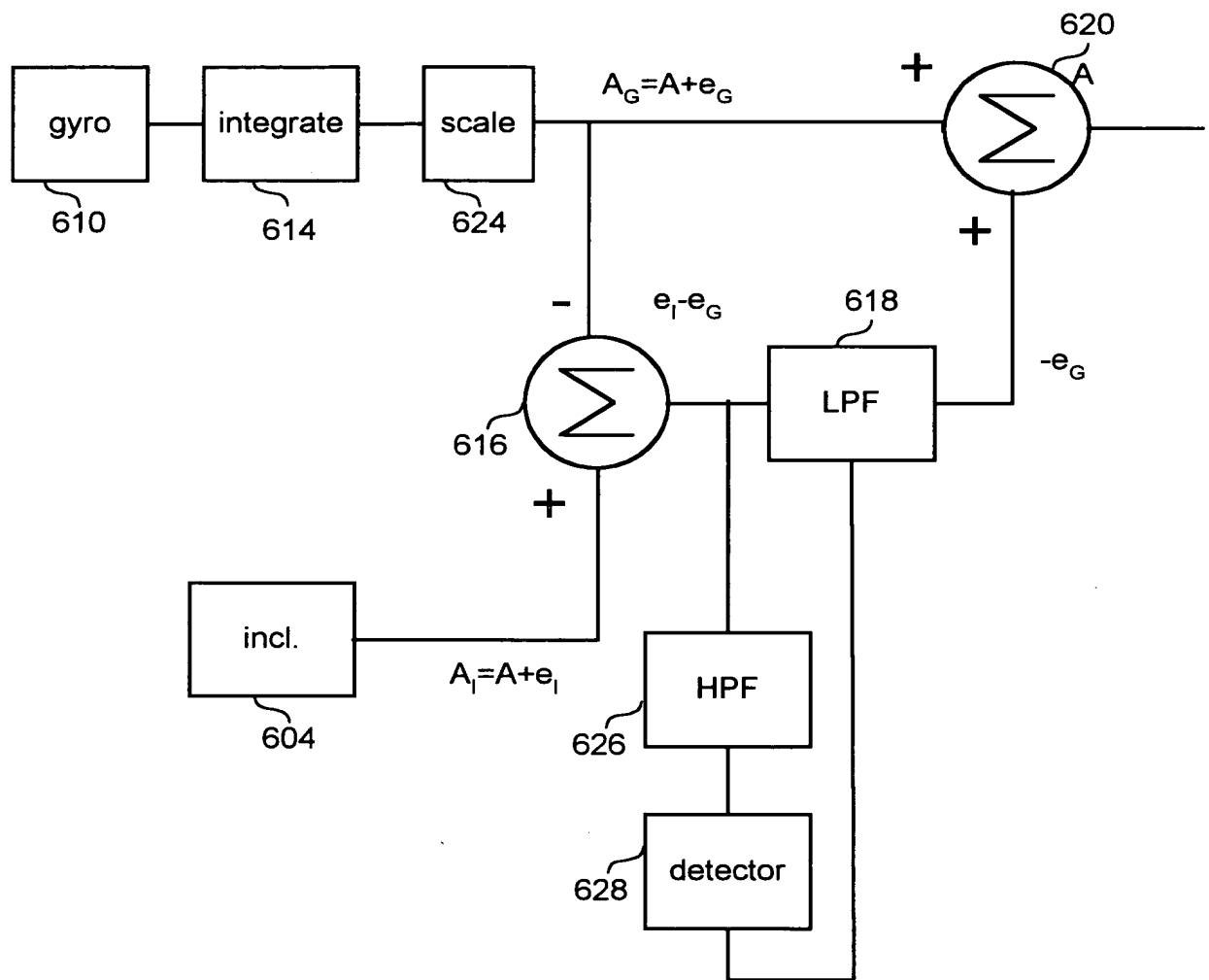


Fig. 9

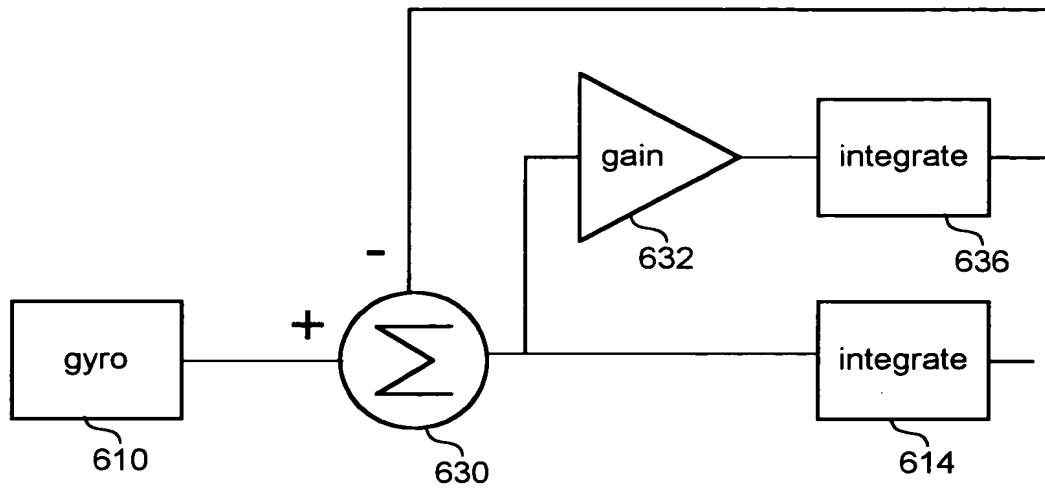


Fig. 10

